



Development of application for plane resistance measurement

1

Problem Statement

To develop an algorithm to measure resistance between any two connected points in a PCB

2

Uniqueness of the requirement

PCB was a multi layer PCB with 24 layers, 2500 points per layer and 1500 interconnects in each layer

3

Solution delivered to the customer

A very comprehensive algorithm based on finite element analysis

4

Value Delivered to customer

The solution incorporated was highly memory optimized. The amount of data processed would actually require 1TB of memory space for processing. Optimization techniques were used to accomplish this with 16 GB memory space.

Customer Profile	Global semiconductor test equipment OEM
Industry Segment	Semiconductor test equipment
Headquarters	Japan
Global Presence	US, China, Singapore, Korea and Europe

Technologies	C# / C++
Tech Areas	Finite element analysis
Engagement	Offshore + Onsite



GES did an extremely good job in development and implementation of the solution. The solutions demanded extensive knowledge of physics and GES pulled it off with ease.



Feasibility test of image processing based odometer testing

1

Problem Statement

To test the feasibility for developing a image processing based solution for sophisticated odometers

Customer Profile

Automotive component and peripherals supplier

Industry Segment

Automotive peripherals

Headquarters

Japan

Global Presence

US and Europe (Germany)

2

Uniqueness of the requirement

The component was to be cost effectively developed as it was intended to be integrated as a part of a bigger product

4

Value Delivered to customer

The PoC was developed in a very competent timeframe

Technologies

WPF and C#

Tech Areas

Image processing

Engagement

Offshore

3

Solution delivered to the customer

A PoC demonstration that proved the feasibility of building such an application



We really appreciate the ability of GES to have built a concept model for this requirement in matter of weeks.